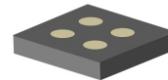


CMSBN4612-HF

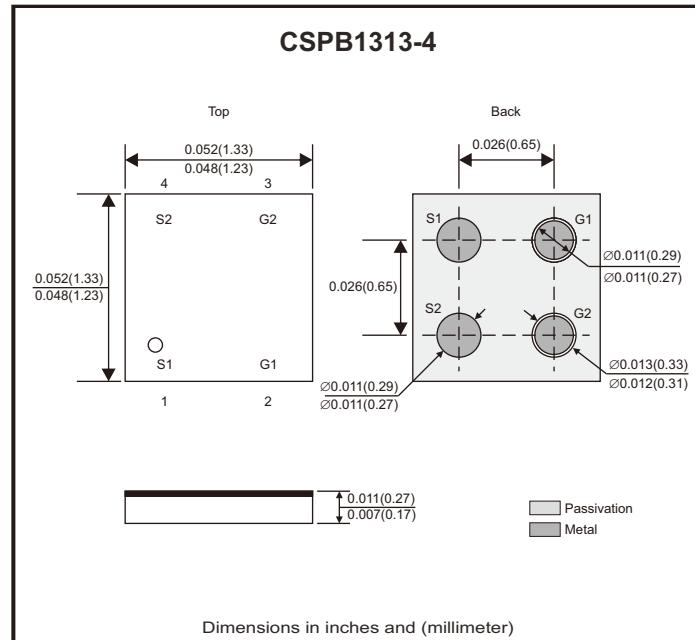
Dual N-Channel
RoHS Device
Halogen Free

**Features**

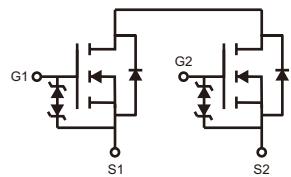
- It is ESD protected.
- This device is suitable for use as a unidirectional or bi-directional load switch, facilitated by its common-drain configuration.

Mechanical data

- Case: CSPB1313-4, standard package, molded plastic.

**Circuit diagram**

- G : Gate
 - S : Source

**Maximum Ratings** (at $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Source to source voltage	V_{SSS}	22	V
Gate-source voltage	V_{GSS}	± 12	V
Source current DC (Note 1)	I_S	6	A
Source current pulse (Note 1, 2)	I_{SP}	60	A
Total dissipation (Note 1)	P_T	1.4	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature range	T_{STG}	-55 to +150	

Notes: 1. Mounted on FR4 board (25.4mm x 25.4mm x t1.0mm) using the minimum recommended pad size (36 μm copper).

2. $t = 10\mu\text{s}$, duty cycle $\leq 1\%$

Electrical Characteristics (at $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Static parameters						
Source to source breakdown voltage	BV_{SS}	$I_S = 250\mu\text{A}, V_{GS} = 0\text{V}$	22			V
Zero-gate voltage source current	I_{SS}	$V_{SS} = 20\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate to source leakage current	I_{GS}	$V_{SS} = 0\text{V}, V_{GS} = \pm 8\text{V}$			± 10	μA
Cutoff voltage	$V_{GS(\text{off})}$	$V_{SS} = 10\text{V}, I_S = 250\mu\text{A}$	0.5	0.8	1.3	V
Forward transfer admittance	$ y_{GF} $	$V_{SS} = 10\text{V}, I_S = 3\text{A}$	1	7		S
Static source to source on-resistance	$R_{SS(on)}$	$V_{GS} = 4.5\text{V}, I_S = 3\text{A}$	21	30	36	$\text{m}\Omega$
		$V_{GS} = 4.0\text{V}, I_S = 3\text{A}$	22	31	38	
		$V_{GS} = 3.8\text{V}, I_S = 3\text{A}$	23	32	40	
		$V_{GS} = 3.1\text{V}, I_S = 3\text{A}$	26	35	45	
		$V_{GS} = 2.5\text{V}, I_S = 3\text{A}$	30	42	60	
Turn-on delay time	$t_{d(on)}$	$V_{SS} = 10\text{V}, I_S = 3\text{A}, V_{GS} = 4.5\text{V}$			0.7	μs
Turn-on rise time	t_r				3.8	
Turn-off delay time	$t_{d(off)}$				10	
Turn-off fall time	t_f				10	
Total gate charge	Q_g	$V_{SS} = 10\text{V}, I_S = 6\text{A}, V_{GS} = 4.5\text{V}$			7.2	nC
Diode forward voltage	$V_{F(S-S)}$	$V_{GS} = 0\text{V}, I_S = 1\text{A}$			1.2	V

Rating and Characteristic Curves (CMSBN4612-HF)

Fig.1 - Output Characteristics

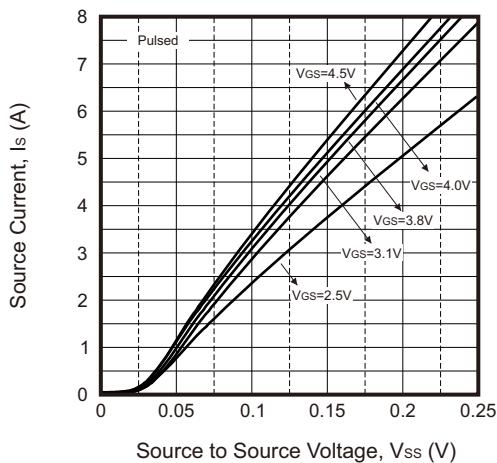
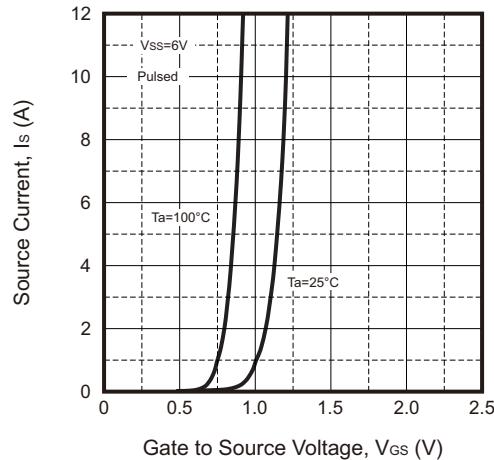


Fig.2 - $I_S — V_{GS}$



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REV:A

CSP Enhancement Mode Power MOSFET

Comchip
SMD Diode Specialist

Rating and Characteristic Curves (CMSBN4612-HF)

Fig.3 - $R_{SS(ON)}$ — I_S

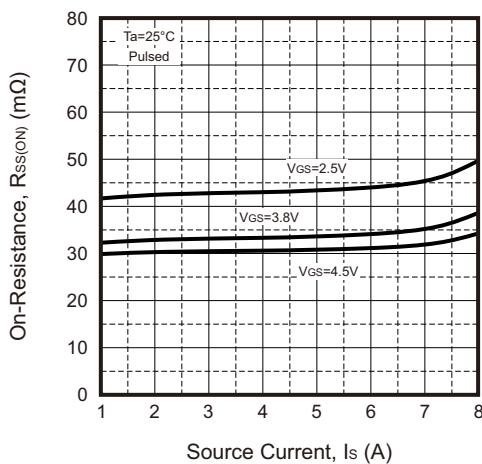


Fig.4 - $R_{SS(ON)}$ — V_{GS}

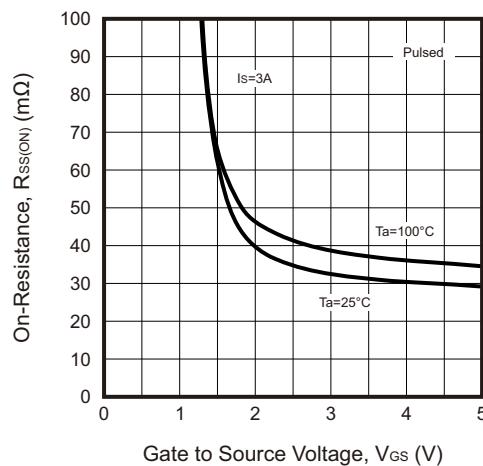


Fig.5 - I_S — V_{SS}

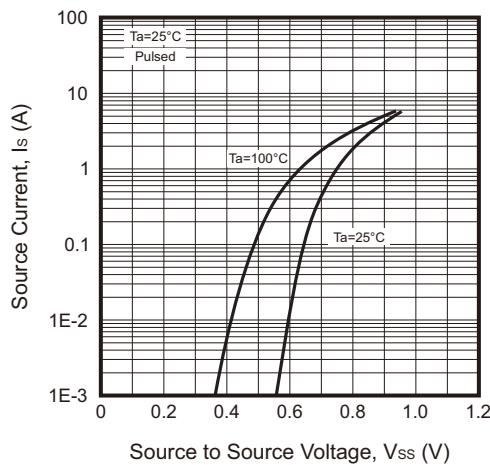


Fig.6 - Threshold Voltage

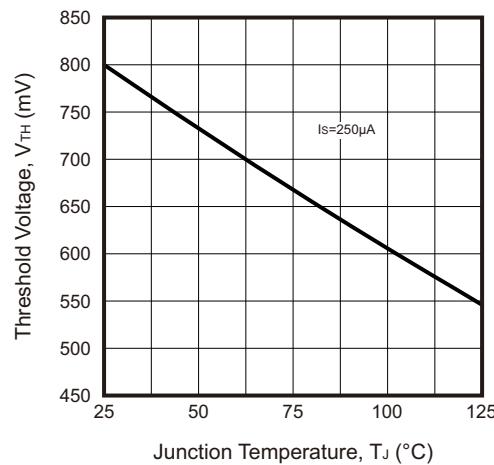


Fig.7 - Total Power Dissipation vs. Ambient Temperature

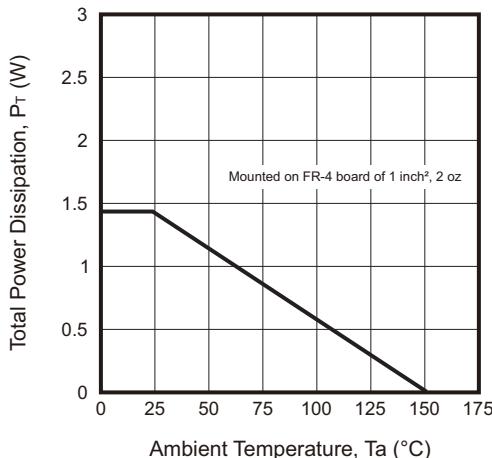
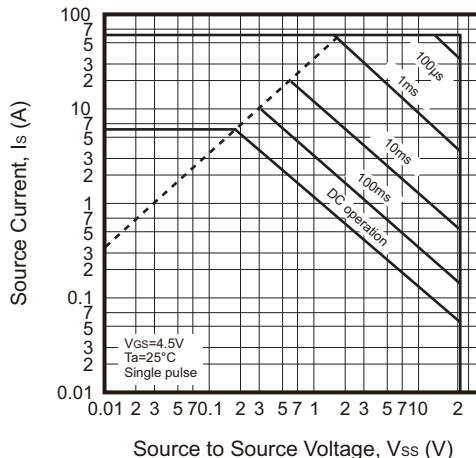


Fig.8 - Maximum Safe Operating Area



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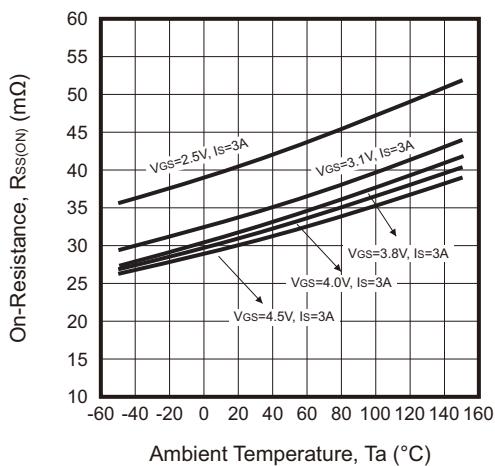
REV:A

CSP Enhancement Mode Power MOSFET

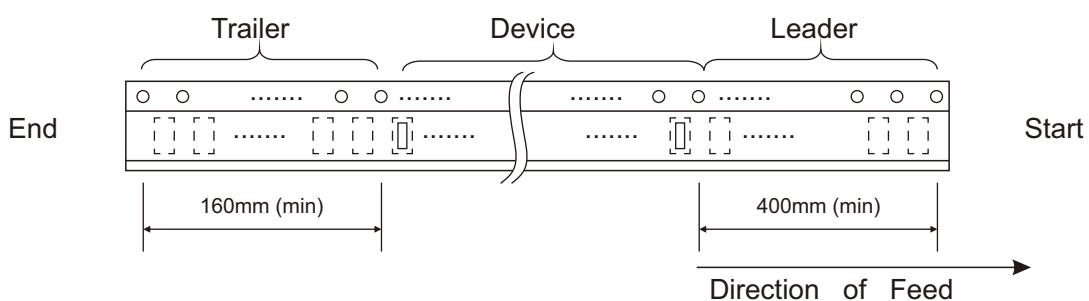
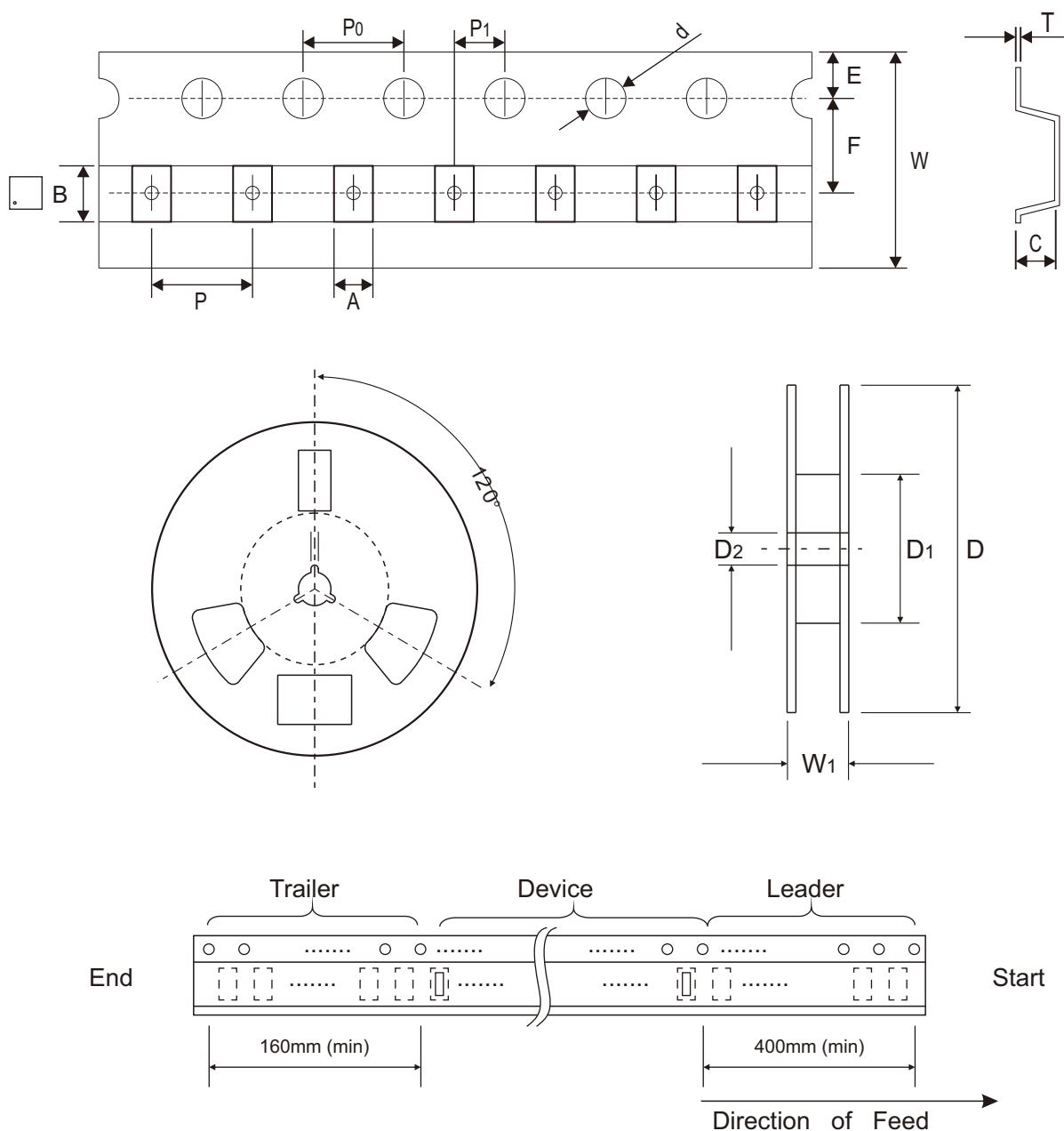
Comchip
SMD Diode Specialist

Rating and Characteristic Curves (CMSBN4612-HF)

Fig.9 - $R_{SS(ON)}$ — T_a



Reel Taping Specification



	SYMBOL	A	B	C	d	D	D ₁	D ₂
CSPB1313-4	(mm)	1.38 ± 0.05	1.38 ± 0.05	0.44 ± 0.05	1.50 ± 0.10	180.00 Typ	60.00 Typ	13.00 Typ
	(inch)	0.054 ± 0.002	0.054 ± 0.002	0.017 ± 0.002	0.059 ± 0.004	7.087 Typ	2.362 Typ	0.512 Typ

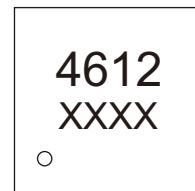
	SYMBOL	E	F	P	P ₀	P ₁	T	W	W ₁
CSPB1313-4	(mm)	1.75 ± 0.10	3.50 ± 0.05	4.00 ± 0.10	4.00 ± 0.10	2.00 ± 0.05	0.20 ± 0.02	8.00 ± 0.30 - 0.10	11.40 Typ
	(inch)	0.069 ± 0.004	0.138 ± 0.002	0.157 ± 0.004	0.157 ± 0.004	0.079 ± 0.002	0.008 ± 0.001	0.315 ± 0.012 - 0.004	0.449 Typ

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REV:A

Marking Code

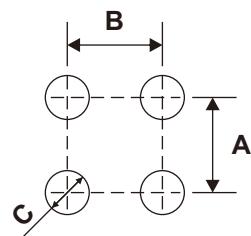
Part Number	Marking Code
CMSBN4612-HF	4612



XXXX = Control code

Suggested PAD Layout

SIZE	CSPB1313-4	
	(mm)	(inch)
A	0.65	0.026
B	0.65	0.026
C	0.30	0.012



Note: 1. The pad layout is for reference purposes only.

Standard Packaging

Case Type	REEL PACK	
	REEL (pcs)	Reel Size (inch)
CSPB1313-4	3000	7