# ANALOG Product/Process Change Notice - PCN 16\_0170 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title:	AD9152 Die Revision and Data Sheet Update		
Publication Date:	01-Sep-2016		
Effectivity Date:	30-Nov-2016	(the earliest date that a customer could expect to receive changed material)	

## **Revision Description:**

Initial Release

## **Description Of Change**

Die Revision Changes: - Rewire charge pump circuitry to increase the initial charge pump voltage.

#### **Reason For Change**

Die Revision Changes:

- The change addresses an issue where at cold temperatures, the on-chip phase-locked loops (PLLs) for either the SERDES or DAC clock PLLs may not lock from startup due to a low initial charge pump voltage.

## Impact of the change (positive or negative) on fit, form, function & reliability

There is no change to the fit, form and reliability of the devices.

The functionality is improved with the revision when attempting to lock the SERDES and/or DAC PLLs at cold temperatures. The SERDES or DAC clock PLL will always lock at cold temperatures.

#### Product Identification (this section will describe how to identify the changed material)

The Revision ID Register Address 0x06 default value changes from 0x07 to 0x08 for silicon revision traceability. Changes will be reflected in the Rev. A of the AD9152 data sheet.

## Summary of Supporting Information

Due to minor nature of the change, no qualification testing is required.

#### Comments

New optimized register settings for PLL programming will be listed in the required SERDES PLL Settings and required DAC PLL Settings tables in the text of the datasheet.

Supporting Documents None

Foi	For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.				
Americas:	PCN_Americas@analog.com	Europe:	PCN_Europe@analog.com	Japan:	PCN_Japan@analog.com

Appendix A - Affected ADI Models				
Added Parts On This Revision - Product Family / Model Number (2)				
AD9152 / AD9152BCPZ	AD9152/AD9152BCPZRL			

Appendix B - Revision History				
Rev	Publish Date	Effectivity Date	Rev Description	
Rev	01-Sep-2016	30-Nov-2016	Initial Release	

Analog Devices, Inc.

Docld:3794 Parent Docld:None Layout Rev:7