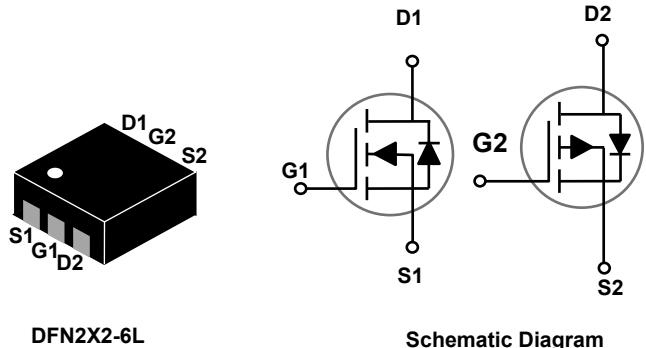


Main Product Characteristics

Polarity	N-Ch	P-Ch
V _{DSS}	12V	-12V
R _{DS(ON)(Max.)}	32mΩ@ V _{GS} =4.5V	74mΩ@ V _{GS} =-4.5V
I _D	5.0A	-5.0A



Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The SSFB12N05 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V _{DS}	12	-12	V
Gate-Source Voltage	V _{GS}	±12	±12	V
Continuous Drain Current	T _A =25°C	I _D	5	-5
	T _A =70°C		4.5	-3.8
Pulsed Drain Current ¹	I _{DM}	20	-15	A
Maximum Power Dissipation	T _A =25°C	P _D	1.9	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	N-Channel	P-Channel	Unit
Thermal Resistance, Junction-to-Ambient ²	R _{θJA}	65	65	°C/W

N-CH Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_d=250\mu\text{A}$	12	20	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=12\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics³						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_d=250\mu\text{A}$	0.4	0.6	1	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_d=5\text{A}$	-	28	32	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_d=4.6\text{A}$	-	36	42	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=1.8\text{V}, \text{I}_d=4.1\text{A}$	-	55	80	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_d=5\text{A}$	-	20	-	S
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=6\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{F}=1.0\text{MHz}$	-	495	-	PF
Output Capacitance	C_{oss}		-	155	-	PF
Reverse Transfer Capacitance	C_{rss}		-	95	-	PF
Switching Characteristics⁴						
Turn-on Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{DD}}=6\text{V}, \text{R}_L=1.2\Omega, \text{V}_{\text{GS}}=10\text{V}, \text{R}_{\text{GEN}}=4.5\Omega$	-	7	-	nS
Turn-on Rise Time	t_r		-	5	-	
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$		-	18	-	
Turn-Off Fall Time	t_f		-	6	-	
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=6\text{V}, \text{I}_d=5\text{A}, \text{V}_{\text{GS}}=4.5\text{V}$	-	6.6	-	nC
Gate-Source Charge	Q_{gs}		-	1	-	
Gate-Drain Charge	Q_{gd}		-	1.2	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=5\text{A}$	-	-	1.2	V

P-CH Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-12	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=-12\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	-1	μA
Gate-Body Leakage Current	I_{GS}	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics³						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=-250\mu\text{A}$	-0.4	-0.7	-1	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-4.5\text{A}$	-	60	74	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-2.5\text{V}, \text{I}_D=-3.2\text{A}$	-	84	110	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-1.8\text{V}, \text{I}_D=-1\text{A}$	-	130	220	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_D=-5\text{A}$	-	10	-	S
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=-6\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{F}=1.0\text{MHz}$	-	520	-	PF
Output Capacitance	C_{oss}		-	100	-	PF
Reverse Transfer Capacitance	C_{rss}		-	65	-	PF
Switching Characteristics⁴						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=-6\text{V}, \text{R}_L=2.3\Omega, \text{V}_{\text{GS}}=-10\text{V}, \text{R}_{\text{GEN}}=6\Omega$	-	7.5	-	nS
Turn-on Rise Time	t_r		-	5.5	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	19	-	
Turn-Off Fall Time	t_f		-	7	-	
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=-6\text{V}, \text{I}_D=-4.5\text{A}, \text{V}_{\text{GS}}=-4.5\text{V}$	-	9.2	-	nC
Gate-Source Charge	Q_{gs}		-	1.6	-	
Gate-Drain Charge	Q_{gd}		-	2.2	-	
Drain-Source Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=-5\text{A}$	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

N-CH Typical Electrical and Thermal Characteristic Curves

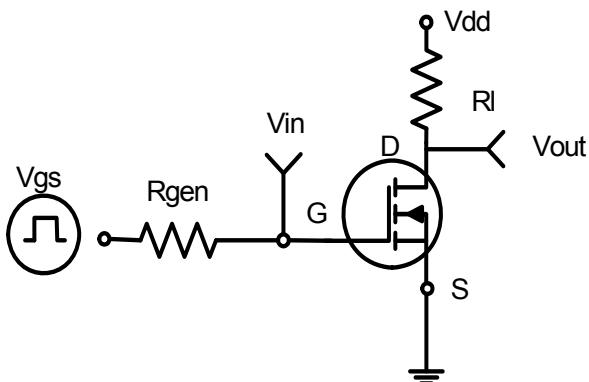


Fig.1 Switching Test Circuit

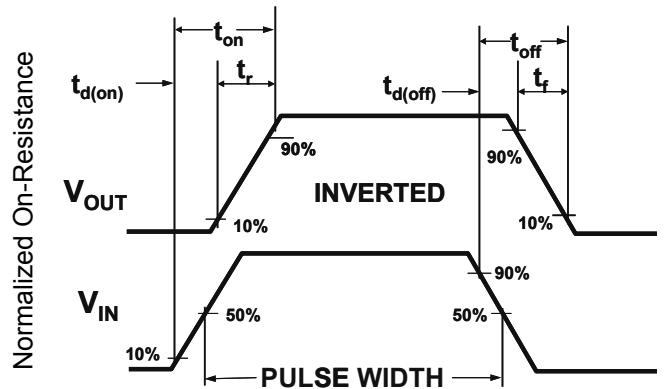


Fig.2 Switching Waveforms

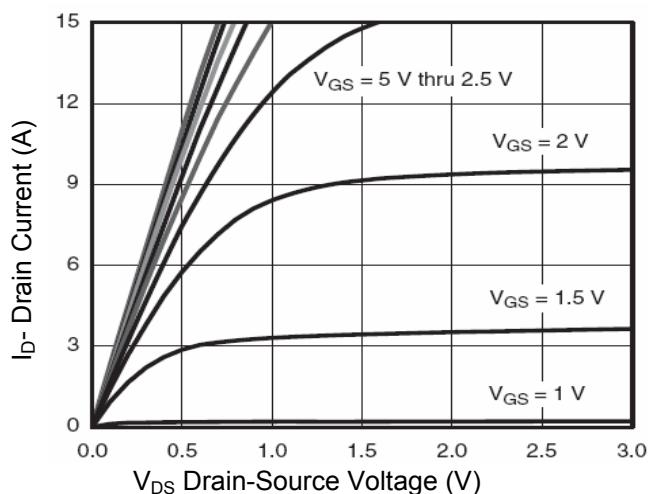


Fig.3 Output Characteristics

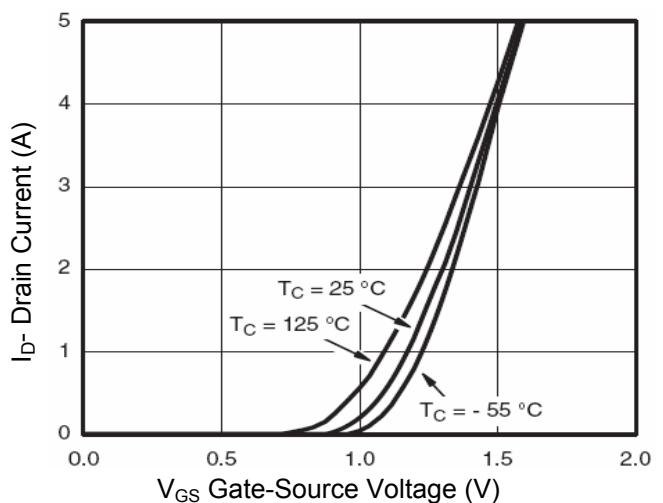


Fig.4 Transfer Characteristics

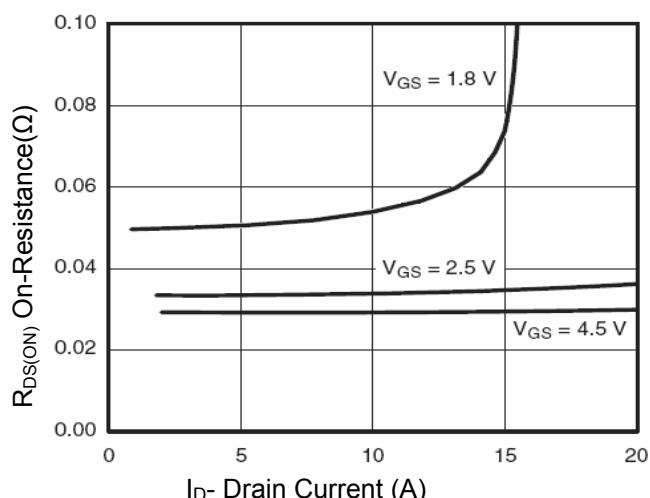


Fig.5 Drain-Source On-Resistance

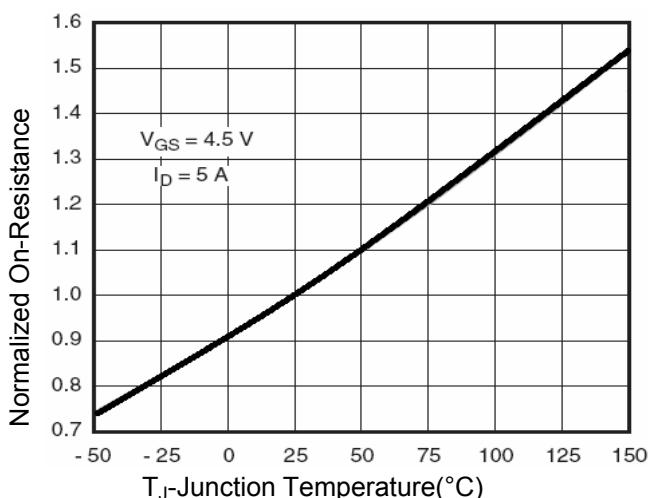


Fig.6 Drain-Source On-Resistance

N-CH Typical Electrical and Thermal Characteristic Curves

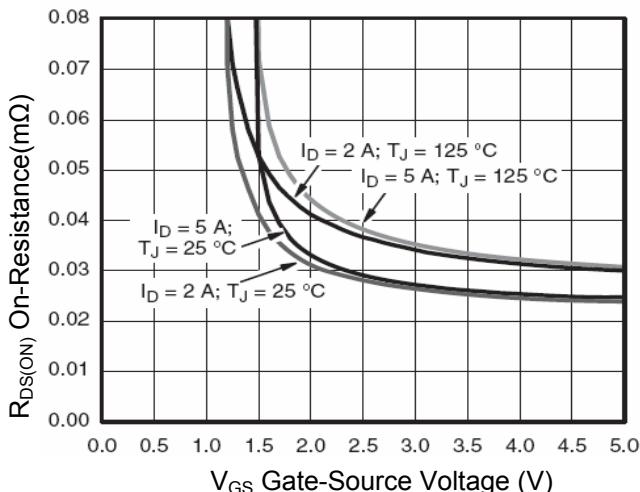


Fig.7 R_{DSON} vs V_{GS}

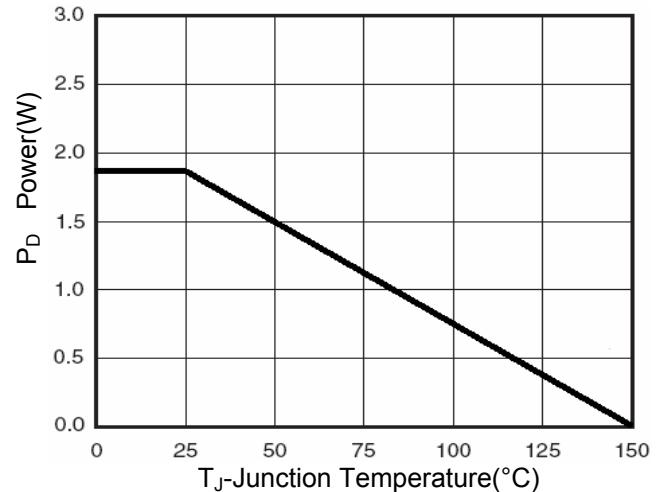


Fig.8 Power Dissipation

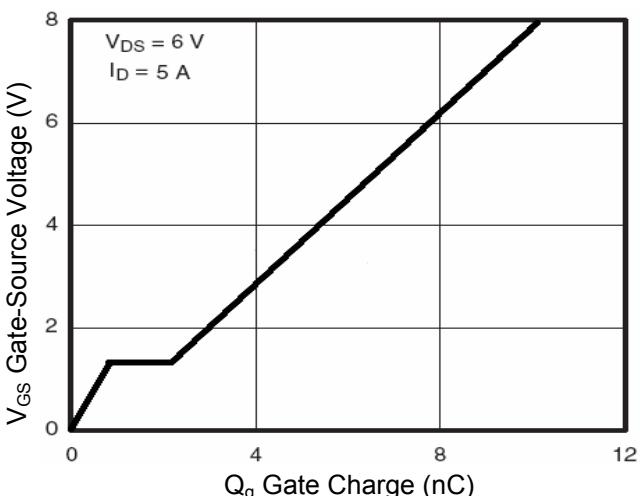


Fig.9 Gate Charge

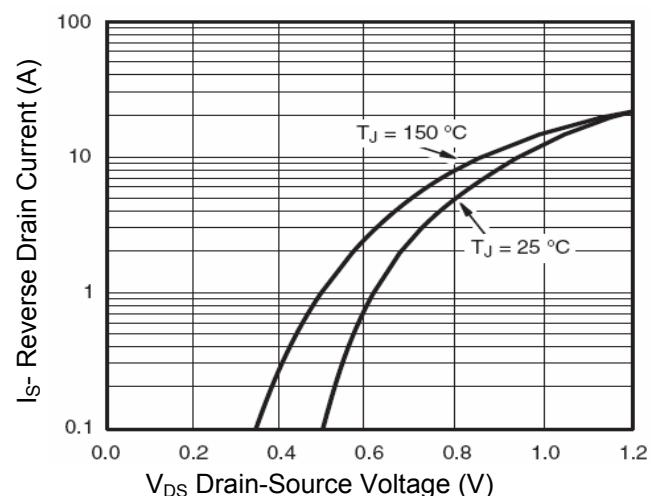


Fig.10 Source- Drain Diode Forward

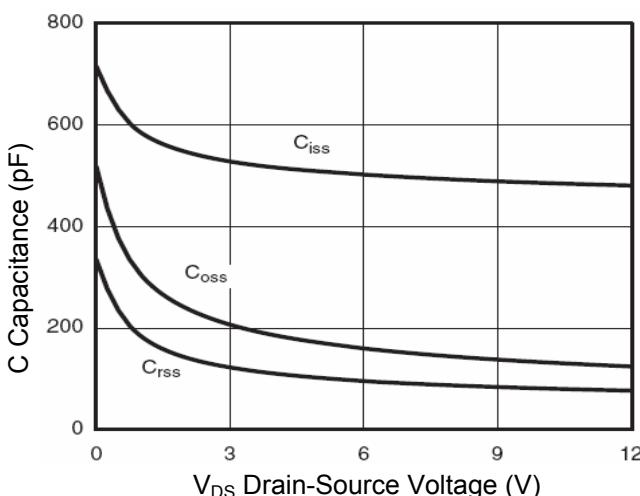


Fig.11 Capacitance vs V_{DS}

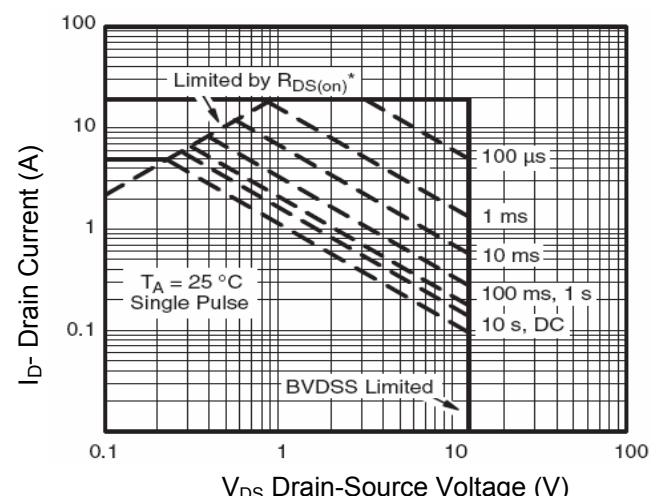


Fig.12 Safe Operation Area

N-CH Typical Electrical and Thermal Characteristic Curves

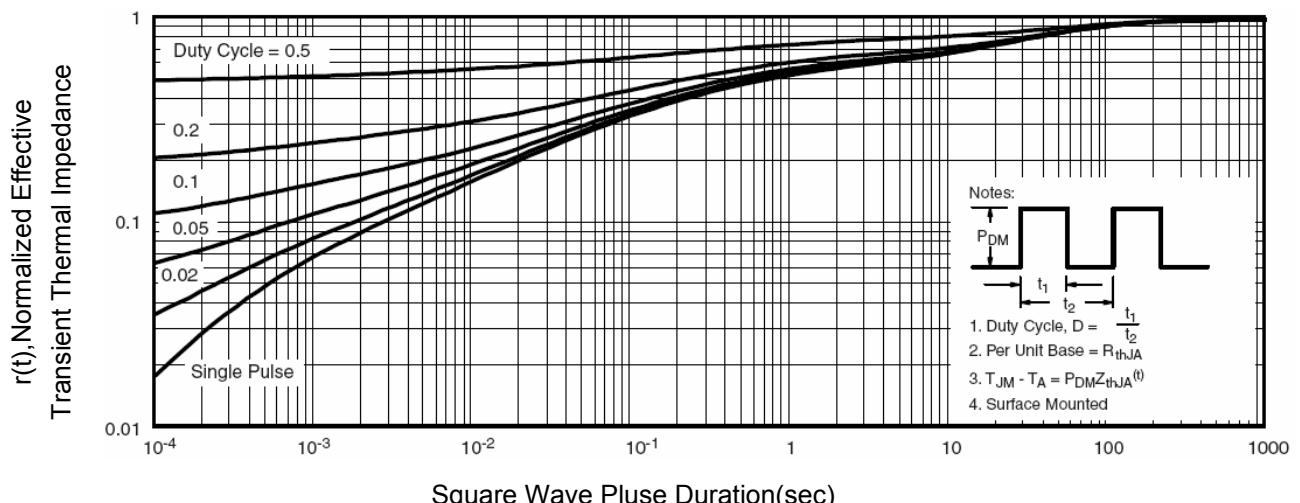


Fig.13 Normalized Maximum Transient Thermal Impedance

P-CH Typical Electrical and Thermal Characteristic Curves

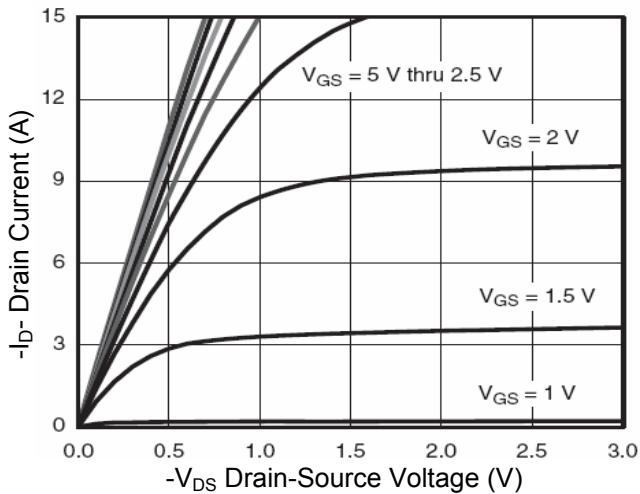


Fig.14 Output Characteristics

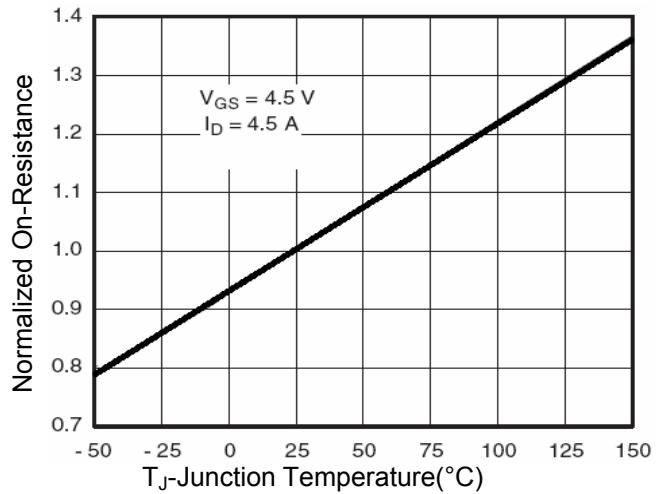


Fig.15 $R_{DS(ON)}$ -Junction Temperature

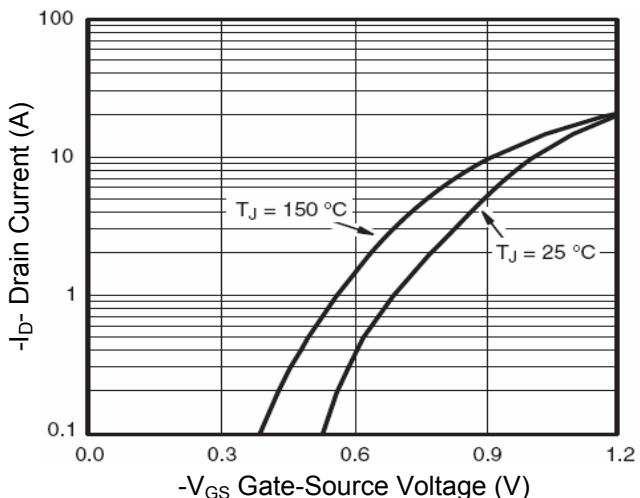


Fig.16 Transfer Characteristics

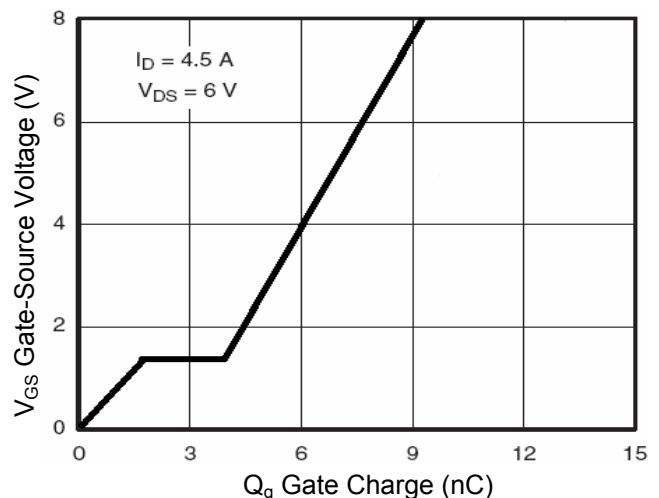


Fig.17 Gate Charge

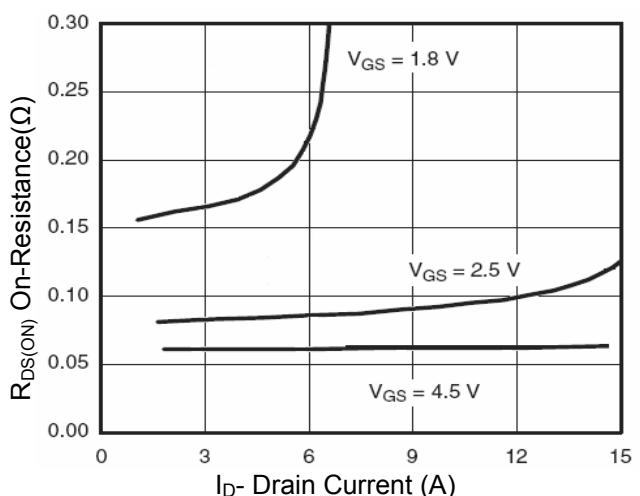


Fig.18 $R_{DS(ON)}$ -Drain Current

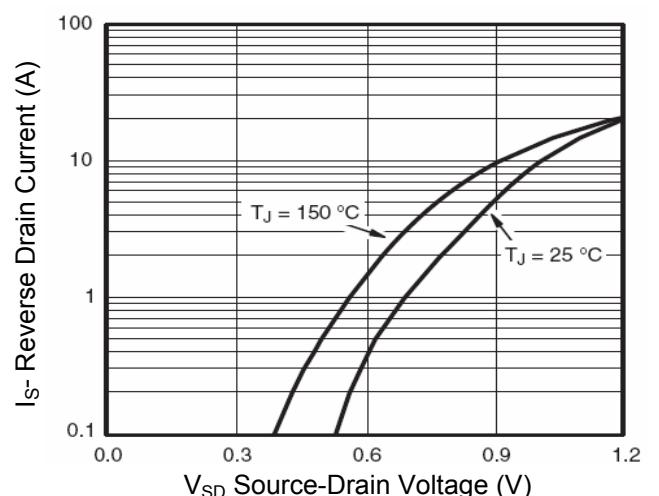


Fig.19 Source-Drain Diode Forward

P-CH Typical Electrical and Thermal Characteristic Curves

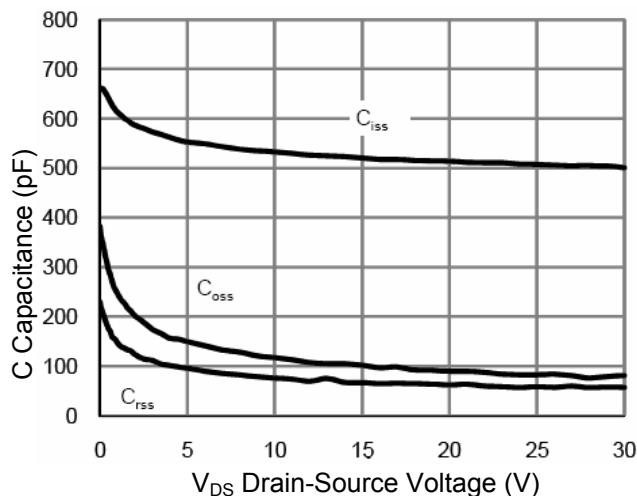


Fig.20 Capacitance vs V_{DS}

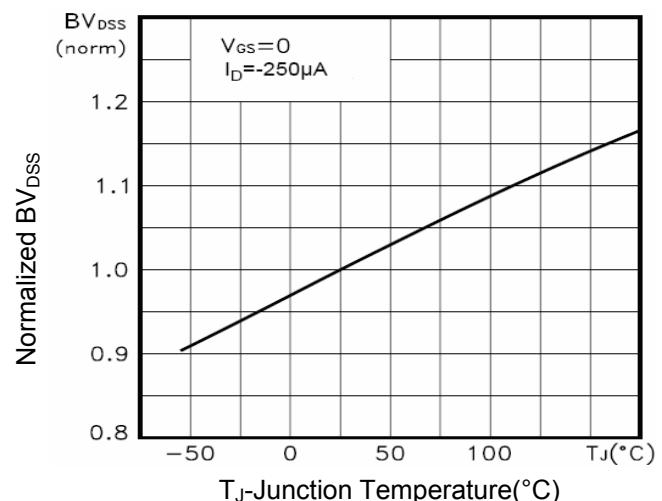


Fig.21 BV_{DSS} vs Junction Temperature

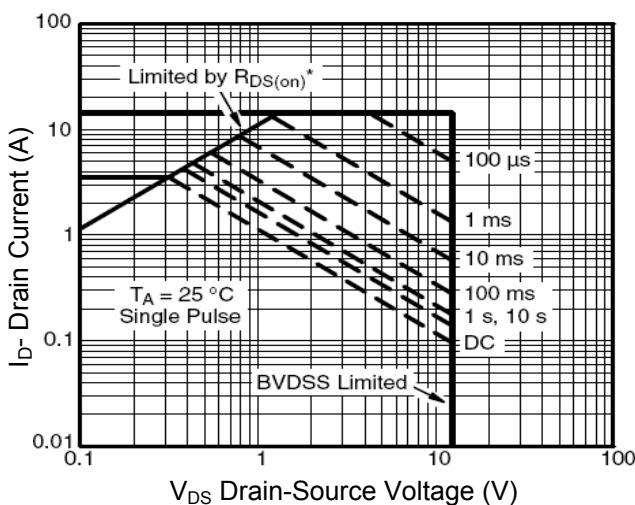


Fig.22 Safe Operation Area

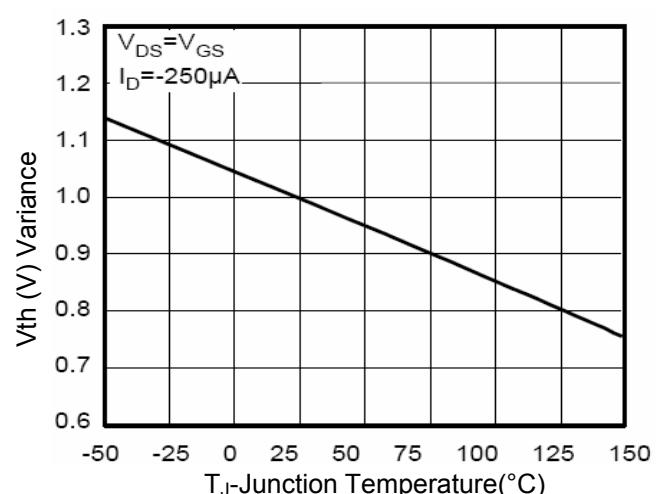


Fig.23 $V_{GS(th)}$ vs Junction Temperature

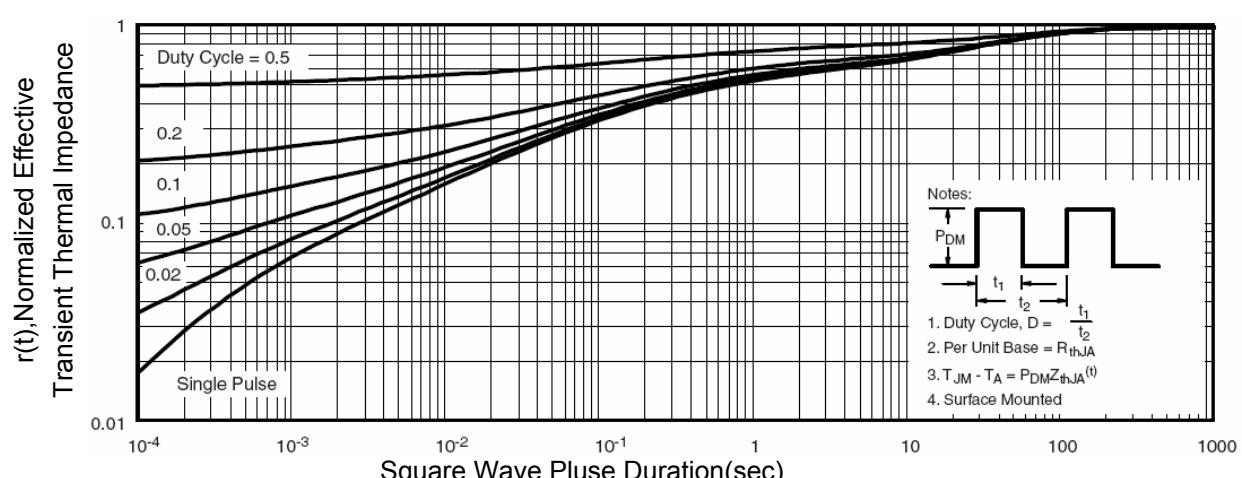
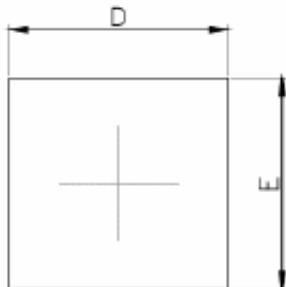


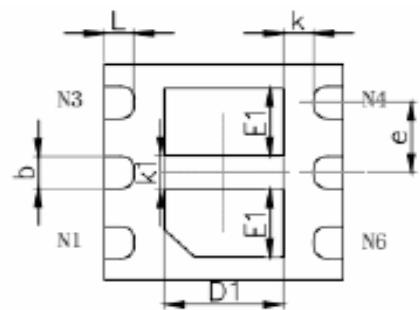
Fig.24 Normalized Maximum Transient Thermal Impedance

Package Outline Dimensions

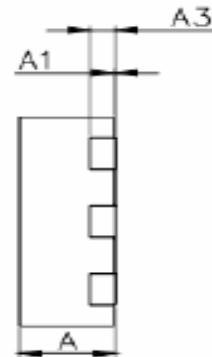
DFN2X2-6L



Top View



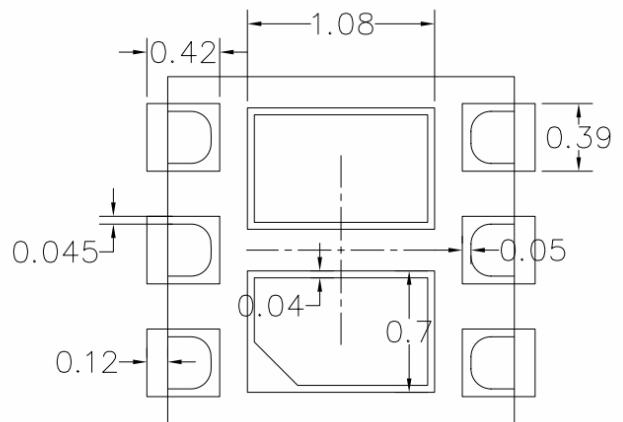
Bottom View



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN.	MAX.	MIN.	MAX.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.900	2.100	0.075	0.083
E	1.900	2.100	0.075	0.083
D1	0.900	1.100	0.035	0.043
E1	0.520	0.720	0.020	0.028
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
k	0.200MIN.		0.008MIN.	
k1	0.320REF		0.013REF.	
L	0.200	0.300	0.008	0.012

Recommended Pad Layout



Note:
 1. Controlling dimensions in mm.
 2. Tolerance $\pm 0.050\text{mm}$.