PCN Number:	2023	0329004.1	D		ator	March 30, 2023	
Title: Qualification of LFAB as an additional Wafer Fab site option for select devices							
Customer Con	tact:	<u>PCN Manager</u>		ept:		Quality Services	
Proposed 1 st S	hip Date:	Jun 29, 2023	Sample F			Apr 29, 2023*	
*Sample requ		after April 29, 2	accepted			•	
	ests received a	inter April 29, 2	025 WIII HOL DE	supp	Jontea.		
Change Type: Assembly S	Cito		22222		Accom	hly Matariala	
	Sile	Assembly Pr Electrical Sp		H	Assembly Materials Mechanical Specification		
Design Test Site		-		HH	Test Process		
Wafer Bur	n Sito	 Packing/Shipping/Labeling Wafer Bump Material 			Wafer Bump Process		
Wafer Fab		Wafer Fab Materials			Water Bump Process Water Fab Process		
	Sile	Part number change					
			Details				
Description of	Change	PCN	Details				
Description of		appounde the	ddition of LEAR a		addition	al Wafer Fab site	
	•	the "Product Affe					
option for the p		the Houdet And	section of		locumen		
	Current Fab Sit	te	Ac	dditio	ditional Fab Site		
Current Fab	Process	Wafer	New Fab	Process		Wafer	
Site	FIOCESS	Diameter	Site		locess	Diameter	
UMC12i	F65	300mm	LFAB		F65	300mm	
Qual details are	provided in the	Qual Data Section	on.				
Reason for Cha	ange:						
Continuity of su	-						
-	<u> </u>						
Anticipated im	pact on Form,	Fit, Function, Q	Quality or Reliab	oility	(positi	ve / negative):	
None							
Changes to pr	oduct identific	ation resulting	from this PCN:				
j p		j					
Device Symbo	l:						
_	7						
<i>i</i> 1	\$\$ = Wafer Fab C	ode (one or two characters)					
· ∀	# = Silicon Revi YM = 2-digit Year/	sion Code					
F280039CSPZ	LLLL = Assembly L	ot Code					
\$\$#-YMLLLLS G4	S = Assembly S	ite Code per QSS 005-120					
Q.	<u>G4</u> = ECAT						
Pin 1							
F ia							
	# = Silicon Revis						
F280039CSPN	YM = 2-digit Year/Month Code F280039CSPN LLLL = Assembly Lot Code						
\$\$#-YMLLLLS							
<u>G4</u>	<u>G4</u> = ECAT						
Pin 1							
	7						
b ia							
		e (one or two characters)					
F280039CSPM		# = Silicon Revision Code XM = 2 digit Xeer/Month Code					
\$\$#-YMLLLLS	LLLL = Assembly Lot						
<u>G4</u>	S = Assembly Site	Code Code per QSS 005-120					
<u>G4</u> २							
<u>G4</u> Q Pin 1	S = Assembly Site						

980 PT F280037CS YMLLLLS \$\$# <u>G4</u> Pin 1	LLLL = Assem S = Assem \$\$ = Wafer I	Year/Month Code				
riginal Fab F $\$\$ = \$7 \rightarrow U$						
pdated Fab Fi $\$\$ = \$7 \rightarrow U$ 0r $\$\$ = 3L \rightarrow L$ Current Fab Si	MC 12i FAB	ation:				
Chip Site Chi		ite Origin Code (20L)	Chip Site Country Code (21L)		Chip Site City	
UMC 12i		UMI SGP		Singapore		
New Chip Site	Chip S			Chip Site Country Co USA	de (21L)	Chip Site City Lehi
ample product	shipping la	bel (not actual pro		label) SN74LS07NSR		
INSTRUMENTS MADE IN: Malaysid 2DC: 20: MSL 2 /260C/1 YE/ MSL 1 /235C/UNLII 0PT: ITEM:	AR SEAL DT		(Q) (31T (4W) ^(P) (2P) F (20L)	2000 (D) 0336)LOT: 3959047MLA TKY(1T) 752348351		
INSTRUMENTS MADE IN: Malaysid 2DC: 20: MSL 2 /260C/1 YE/ MSL 1 /235C/UNLIN OPT: ITEM: LBL: 5A (L)	AR SEAL DT 1 03/29/04 10:1750		(Q) (31T (4W) ^(P) (2P) F (20L)	2000 (D) 0336)LOT: 3959047MLA TKY(1T) 7523483SI REV: (V) 0033317 CSO: SHE (21L) CCO:USA		
INSTRUMENTS MADE IN: Malaysid 2DC: 20: MSL '2 /260C/1 YE/ MSL 1 /235C/UNLIF OPT: ITEM: LBL: 5A (L)	AR SEAL DT 1 03/29/04 TO:1750	80037CSPT	(Q) (31T (4W) ^(P) (2P) F (20L) (22L)	2000 (D) 0336)LOT: 3959047MLA TKY(1T) 7523483SI REV: (V) 0033317 <u>CSO: SHE</u> (21L) CCO:USA ASO: MLA 23L) ACO: MYS		SPN
INSTRUMENTS MADE IN: Malaysid 2DC: 20: MSL 2 /260C/1 YE/ MSL 1 /235C/UNLII 0PT: ITEM:	AR SEAL DT 1 03/29/04 70:1750 ed:	80037CSPT 80037SPM	(Q) (31T (4W) (2P) F (201) (22L) F2	2000 (D) 0336)LOT: 3959047MLA TKY(1T) 7523483SI REV: (V) 0033317 CSO: SHE (21L) CCO:USA ASO: MLA (23L) ACO: MYS 80037SPN	>	

Change Qualification Report Approve Date 28-MARCH -2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Name	Condition	Duration	Qualification Device: <u>F280039CSPZ</u>	Water fab QBS Reference: <u>TMS320F28379SPTPQ</u>
HAST	A2	Biased HAST	130C/85%RH	96 Hours	QBS	3/231/0
UHAST	A3	Unbiased HAST	130C/85%RH	96 Hours	QBS	3/231/0
TC	A4	Temperature Cycling	-65C150C	500 cycles	QBS	3/231/0
HTOL	B1	Life Test	125C	1000 Hours	-	0/231/0
HTOL	B1	Life Test	125C	500 Hours	3/231/0	-
HTSL	B3	High Temperature Storage Life	150C	1000 hours	-	3/231/0
HTSL	B3	High Temperature Storage Life	150C	500 hours	3/231/0	
ESD	E2	ESD CDM	-	500 Volts	1/3/0	
ESD	E2	ESD HBM	-	2000 Volts	1/3/0	
LU	E4	Latch-Up	Per JESD78	-	1/6/0	

QBS: Qual By Similarity

Qual Device F28003xxCSPZ is qualified at MSL3 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

• The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Flash memory was cycled with programming/erasing operations prior to HTOL and HTSL which serves as flash memory data retention tests

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

For questions regarding this notice, e-mails can be sent to the contact shown below or your local Field Sales Representative.

Location	E-Mail				
WW Change Management Team	<u>PCN ww admin team@list.ti.com</u>				

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