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Bulletin Date: 7/10/2015		Bulletin Effective Date:	7/10/2015
Title: Si5347, Si5346, Si5345, Si53	44, Si53	42, Si5341, Si5340 Rev 1.0	Datasheet availability
Originator: Murali Chandran		Phone: (512) 464-9278	Dept: Timing Marketing
Customer Contact: Kathy Haggar		Phone: (512) 532-5261	Dept: Sales
		Bulletin Details	· · · · · · · · · · · · · · · · · · ·
Description: Silicon Labs is pleased to announce products: - Si5347 - Si5346 - Si5345 - Si5344 - Si5342 - Si5341 - Si5340 Datasheet changes are summarized Si5347/46 Updated minimum input frequency spec	d below: Si5345/ Updated	44/42 I minimum input frequency spec	Si5341/40 Updated text and formatting in Tables
to 0.008 MHz. Corrected AC Test Configuration Schematic. Updated FINC/FDEC update rates to 1	to 0.008 MHz. Corrected AC Test Configuration Schematic. Updated FINC/FDEC update rates to 1		2,3,4,5,8,11 and 12. Corrected AC Test Configuration Schematic. Added crosstalk spec for Si5340 to
µs maximum Corrected XAXB VIN_DIFF minimum input voltage swing in Table 3. Modified VIN input voltage swing to be split into VIN_DIFF and VIN_SE for	µs maxi Correcte typical a Added c		Table 5.Updated Vin_diff minimum value in Table 3 to be the same as Vin_se.Updated the PLL lock time in Table 8.
differential and single-ended inputs Added 4-Output Si5347C and Si5347D grade parts to the datasheet. Added common-mode voltage for 1.8 V sub-LVDS in Table 5. Added typical crosstalk spec for Si5346	Correcte swing sp	ed SPI Tr/Tf from Table 10 ed XAXB minimum input voltage bec from 350 to 365 mV. ypical crosstalk spec for Si5342 344.	Added a spec to Table 8 for the VCO frequency range. Updated the "Delay Time Between Chip Selects" to be 2.0 clock periods. Updated Note 2 in Table 12 to state that only 25 and 48–54 Mhz crystals are supported
Updated TSK, output-to-output skew, in Table 5.	split into inputs re	ed INx voltage swing spec and o single-ended and different equirements.	supported. Added a 1.0 µf bypass capacitor recommendation to be consistent with the reference manual. 1.0
Updated ZO, differential output impedance for Low Power Mode in Table 5.		I max IDDOx spec for LVDS rom 17 to 18 mA.	Updated the timing specs for I2C and SPI.
Updated SPI timing diagrams and spec. Corrected tSTART, tACQ, and tRDY in Table 8. Adjusted LVCMOS VOH specification in	Updated output v swing fr	I SPI timing diagrams and spec. I max normal mode LVPECL oltage om 950 to 1000 mVpp_se. I max VCM specs.	

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Bulletin #<u>1507101</u>

Reason:Datasheets now reflect the results of full production characterization data.Product Identification:Affected products can be identified by the following Orderable Part Numbers (OPNs):BASE DEVICES are:- Si5347x-B-GM where x = A, B, C, D- Si5346x-B-GM where x = A, B, C, D- Si5345x-B-GM where x = A, B, C, D- Si5344x-B-GM where x = A, B, C, D- Si5342x-B-GM where x = A, B, C, D- Si5341x-B-GM where x = A, B, C, D- Si5340x-B-GM where x = A, B, C, D- Si5340x-B-GM where x = A, B, C, D- Si5347x-B-GM where x = A, B, C, D- Si5347x-B-GM where x = A, B, C, D- Si5340x-B-GM where x = A, B, C, D- Si5340x-B-GM where x = A, B, C, D- Si5347x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence- Si5344x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence
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 Si5344x-B-GM where x = A, B, C, D Si5342x-B-GM where x = A, B, C, D Si5341x-B-GM where x = A, B, C, D Si5340x-B-GM where x = A, B, C, D <u>CUSTOM FACTORY PRE-PROGRAMMED DEVICES</u> are: Si5347x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence Si5346x-Byyyy-GM where x = A, B and yyyy=custom part number sequence Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence Si5345x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence Si5344x-Byyyy-GM where x = A, B, C, D and yyyy=custom part number sequence
 Si5342x-B-GM where x = A, B, C, D Si5341x-B-GM where x = A, B, C, D Si5340x-B-GM where x = A, B, C, D <u>CUSTOM FACTORY PRE-PROGRAMMED DEVICES</u> are: Si5347x-Byyyyy-GM where x = A, B, C, D and yyyyy=custom part number sequence Si5346x-Byyyyy-GM where x = A, B, C, D and yyyyy=custom part number sequence Si5345x-Byyyyy-GM where x = A, B, C, D and yyyyy=custom part number sequence Si5344x-Byyyyy-GM where x = A, B, C, D and yyyyy=custom part number sequence
 Si5341x-B-GM where x = A, B, C, D Si5340x-B-GM where x = A, B, C, D <u>CUSTOM FACTORY PRE-PROGRAMMED DEVICES</u> are: Si5347x-Byyyyy-GM where x = A, B, C, D and yyyy=custom part number sequence Si5346x-Byyyyy-GM where x = A, B, C, D and yyyy=custom part number sequence Si5345x-Byyyyy-GM where x = A, B, C, D and yyyy=custom part number sequence Si5344x-Byyyyy-GM where x = A, B, C, D and yyyy=custom part number sequence
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- Si5342x-Byyyyy-GM where x = A, B, C, D and yyyyy=custom part number sequence
- Si5341x-Byyyyy-GM where $x = A, B, C, D$ and yyyyy=custom part number sequence
- Si5340x-Byyyyy-GM where x = A, B, C, D and yyyyy=custom part number sequence. This change is considered a minor change which does not affect form, fit, function, quality, or
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